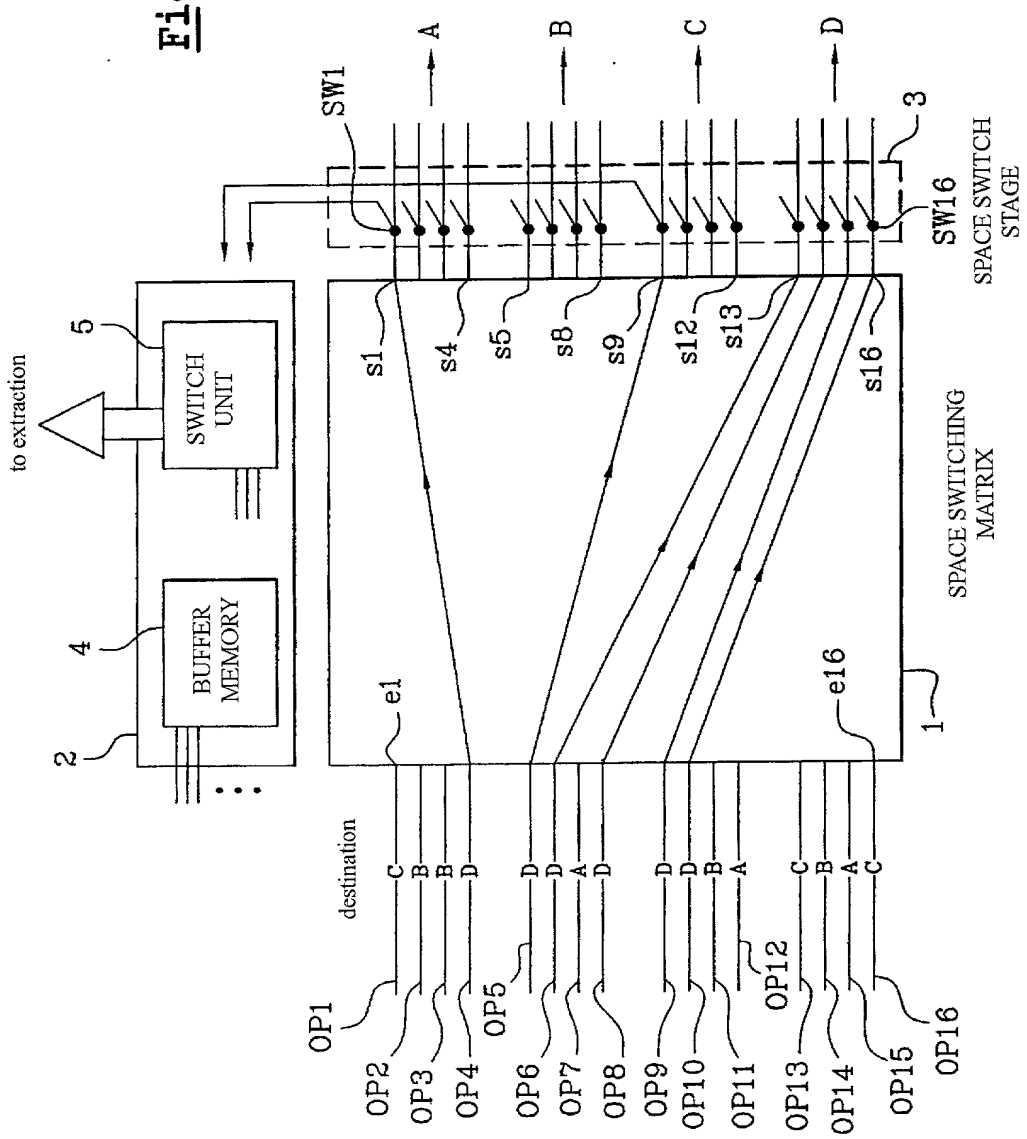
1/1

Fig.1


SW1
A
B
C
D
3
SW16
SPACE SWITCH STAGE
5
SWITCH UNIT
to extraction
4
BUFFER MEMORY
2
s1
s4
s5
s8
s9
s12
s13
s16
SPACE SWITCHING MATRIX
e1
e16
1
destination
OP1
OP2
OP3
OP4
OP5
OP6
OP7
OP8
OP9
OP10
OP11
OP12
OP13
OP14
OP15
OP16